

DIM1200FSM12-A000

Single Switch IGBT Module

Replaces DS5547-4 DS5547-5 July 2014 (LN31765)

FEATURES

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- Non Punch Through Silicon
- Isolated AlSiC Base with AlN Substrates
- Lead Free construction

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM1200FSM12-A000 is a single switch 1200V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM1200FSM12-A000

Note: When ordering, please use the complete part number

KEY PARAMETERS

V _{CES}		1200V
V _{CE(sat)}	* (typ)	2.2V
l _c ` ´	(max)	1200A
I _{C(PK)}	(max)	2400A

^{*} Measured at the power busbars, not the auxiliary terminals

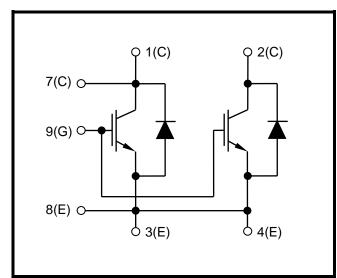


Fig. 1 Circuit configuration



Fig. 2 Package

ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	V _{GE} = 0V	1200	V
V_{GES}	Gate-emitter voltage		±20	V
I _C	Continuous collector current	T _{case} = 85°C	1200	Α
I _{C(PK)}	Peak collector current	1ms, T _{case} = 110°C	2400	Α
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$	10400	W
l ² t	Diode I ² t value	$V_R = 0, t_p = 10 \text{ms}, T_j = 125 ^{\circ}\text{C}$	400	kA ² s
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	2500	V
Q_{PD}	Partial discharge – per module	IEC1287, $V_1 = 1300V$, $V_2 = 1000V$, 50Hz RMS	10	рC

THERMAL AND MECHANICAL RATINGS

Internal insulation material:

Baseplate material:

Creepage distance:

Clearance:

CTI (Comparative Tracking Index):

AIN

AISiC

20mm

10mm

>600

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R _{th(j-c)}	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	12	°C/kW
R _{th(j-c)}	Thermal resistance – diode	Continuous dissipation - junction to case	-	-	20	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	8	°C/kW
Tj	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	125	°C
T _{stg}	Storage temperature range	-	-40	-	125	°C
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm

ELECTRICAL CHARACTERISTICS

 T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
I _{CES}	Collector cut-off current	$V_{GE} = 0V$, $V_{CE} = V_{CES}$			1.5	mA
		$V_{GE} = 0V$, $V_{CE} = V_{CES}$, $T_{case} = 125$ °C			40	mA
I _{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			6	μΑ
V _{GE(TH)}	Gate threshold voltage	$I_C = 60$ mA, $V_{GE} = V_{CE}$	4.5	5.5	6.5	V
	Collector-emitter saturation voltage	V _{GE} = 15V, I _C = 1200A		2.2	2.8	V
V _{CE(sat)}		V _{GE} = 15V, I _C = 1200A, T _j = 125°C		2.6	3.2	V
I _F	Diode forward current	DC			1200	Α
I _{FM}	Diode maximum forward current	t _p = 1ms			2400	Α
.,,	Diode forward voltage	I _F = 1200A		1.9	2.1	V
V_{F}		I _F = 1200A, T _j = 125°C		1.8	2.1	V
C _{ies}	Input capacitance	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz		135		nF
Qg	Gate charge	±15V		12		μC
C _{res}	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$				nF
L _M	Module inductance			15		nΗ
R _{INT}	Internal transistor resistance			140		μΩ
SC _{Data}	Short circuit current, I _{SC}	$T_{j} = 125^{\circ}C, V_{CC} = 900V$ $t_{p} \le 10\mu s, V_{GE} \le 15V$ $V_{CE (max)} = V_{CES} - L^{*}x dI/dt$ IEC 60747-9		7000		А

Note:

L is the circuit inductance + L_M

ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	$I_{C} = 1200A$ $V_{GF} = \pm 15V$		1250		ns
t _f	Fall time			180		ns
E _{OFF}	Turn-off energy loss	$V_{CE} = 600V$		160		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = 2.2\Omega$ $R_{G(OFF)} = 2.2\Omega$ $L_S \sim 100 \text{nH}$		250		ns
t _r	Rise time			200		ns
E _{ON}	Turn-on energy loss			80		mJ
Q_{rr}	Diode reverse recovery charge	I _F = 1200A V _{CE} = 600V		120		μC
I _{rr}	Diode reverse recovery current			570		Α
E _{rec}	Diode reverse recovery energy	$dI_F/dt = 6200A/\mu s$		60		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time			1380		ns
t _f	Fall time	$I_C = 1200A$ $V_{GE} = \pm 15V$		200		ns
E _{OFF}	Turn-off energy loss	$V_{GE} = £13V$ $V_{CE} = 600V$		260		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = 2.2\Omega$ $R_{G(OFF)} = 2.2\Omega$ $L_{S} \sim 100 \text{nH}$		350		ns
t _r	Rise time			220		ns
E _{ON}	Turn-on energy loss			140		mJ
Q_{rr}	Diode reverse recovery charge	$I_F = 1200A$ $V_{CE} = 600V$ $dI_F/dt = 5700A/\mu s$		240		μC
I _{rr}	Diode reverse recovery current			680		Α
E _{rec}	Diode reverse recovery energy			110		mJ

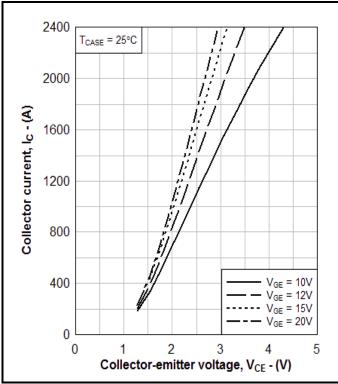


Fig. 3 Typical output characteristics

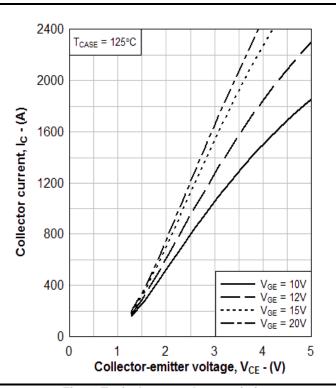


Fig. 4 Typical output characteristics

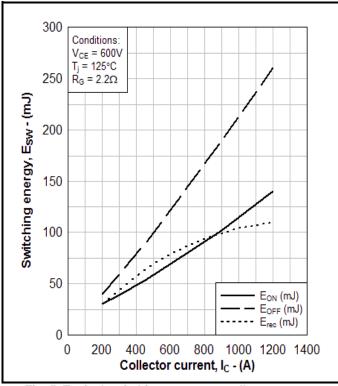


Fig. 5 Typical switching energy vs collector current

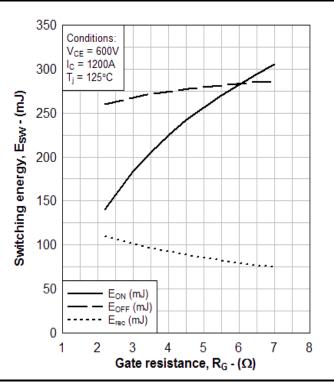


Fig. 6 Typical switching energy vs gate resistance

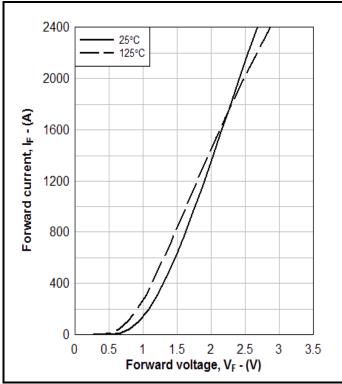


Fig. 7 Diode typical forward characteristics

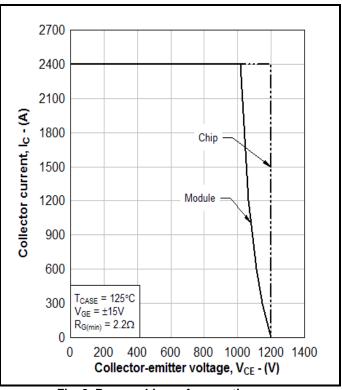


Fig. 8 Reverse bias safe operating area

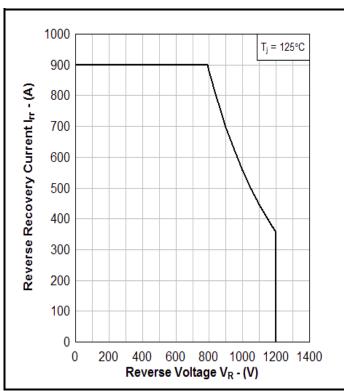


Fig. 9 Diode reverse bias safe operating area

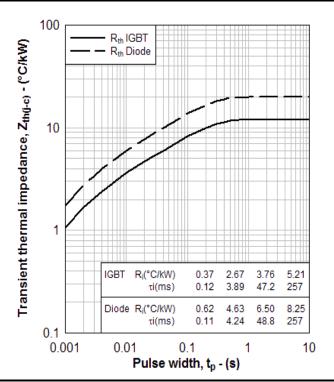


Fig. 10 Transient thermal impedance

PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

DO NOT SCALE.

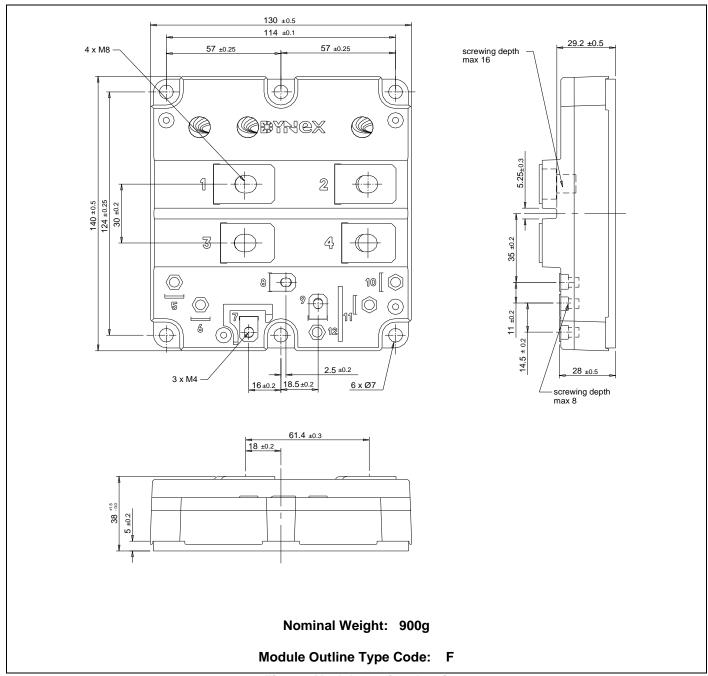


Fig. 11 Module outline drawing

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